

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Currently Amended) An electronic device comprising:

 ~~means for inhibiting activation of the electronic device~~ a switch which,

 when turned on, inhibits activation of the electronic device;

 means for holding information representing whether activation of the

 electronic device is inhibited by the ~~inhibiting means~~ switch; and

 means for executing activation or activation inhibition of the electronic

 device on the basis of the information stored in the holding means

 when an activation instruction is generated in the electronic device.

2. (Currently Amended) A device according to claim 1, wherein the ~~inhibiting means~~

 switch is arranged in a housing of the electronic device.

3. (Previously Presented) A device according to claim 1, wherein the executing means

 includes a sub-processor different from a main processor arranged in the

 electronic device.

4. (Previously Presented) A device according to claim 1, wherein the executing means

 includes a sub-processor different from a main processor arranged in the

 electronic device, and the holding means is arranged in the sub-processor.

5. (Currently Amended) A device according to claim 2, wherein the ~~inhibiting means~~ switch designates inhibition even when the electronic device is OFF.
6. (Previously Presented) A device according to claim 3, which further comprises a power supply controller that manages a power supply of the electronic device, and in which the executing means issues a power supply request to the power supply controller when the activation instruction is received and activation of the electronic device is permitted.
7. (Currently Amended) An electronic device comprising:
~~means for inhibiting activation of the electronic device~~ a switch which,
when turned on, inhibits activation of the electronic device; and
means for, when an activation instruction is generated from software which runs in the electronic device, deciding whether activation of the electronic device is inhibited based on information representing whether activation of the electronic device is inhibited by the ~~inhibiting means~~ switch, and when activation is inhibited, inhibiting activation of the electronic device.
8. (Currently Amended) A device according to claim 7, wherein the ~~inhibiting means~~ switch is arranged in a housing of the electronic device.

9. (Previously Presented) A device according to claim 7, wherein the deciding means includes a sub-processor different from a main processor arranged in the electronic device.
10. (Currently Amended) A device according to claim 8, wherein the ~~inhibiting means~~ switch designates inhibition even when the electronic device is OFF.
11. (Previously Presented) A device according to claim 7, which further comprises a power supply controller that manages a power supply of the electronic device, and in which the deciding means issues a power supply request to the power supply controller when the activation instruction is received and activation of the electronic device is permitted.
12. (Currently Amended) An electronic device comprising:
~~means for inhibiting activation of the electronic device~~ a switch which,
when turned on, inhibits activation of the electronic device;
means for holding information representing whether activation is inhibited
by the ~~inhibiting means~~ switch; and
means for executing activation or activation inhibition of the electronic device on the basis of the information stored in the ~~storage~~ holding
means upon reception of one of an activation instruction from a power supply switch arranged in a housing of the electronic device, an activation instruction from software which runs in the electronic

device, and an activation instruction from a network connected to the electronic device.

13. (Currently Amended) A power supply control method in an electronic device, comprising:

storing information representing activation inhibition or activation permission of the electronic device that is designated by ~~inhibiting~~
~~means for inhibiting~~ a switch which, when turned ON, inhibits
activation of the electronic device;
receiving an activation instruction for the electronic device from software which runs in the electronic device; and
inhibiting activation of the electronic device when the stored information represents activation inhibition.

14. (Original) A method according to claim 13, wherein storage of the information, reception of the activation instruction, and activation inhibition of the electronic device are performed by a sub-processor.

15. (Original) A method according to claim 13, wherein
a power supply controller which manages a power supply of the electronic device is further arranged, and

a power supply request is issued to the power supply controller when the activation instruction for the electronic device is received and the information permits activation.

16. (Currently Amended) A power supply control method in an electronic device having ~~inhibiting means for inhibiting~~ a switch which, when turned on, inhibits activation of the electronic device, and storage means for holding information representing whether activation is inhibited by the ~~inhibiting means~~ switch, comprising:
- receiving an activation instruction from a power supply switch arranged in a housing of the electronic device, an activation instruction from software which runs in the electronic device, or an activation instruction from a network connected to the electronic device; and executing activation or activation inhibition of the electronic device on the basis of the information stored in the storage means.